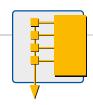


Advanced Development Package for your complex JTAG/Boundary Scan Projects





- Complete hardware and software package for IEEE 1149.x
- Graphical project development supported by Mission Assist™
- Automated test generation and pin-failure diagnosis
- Debug of BGA pins and embedded nets
- Support of all Embedded System Access (ESA) technologies

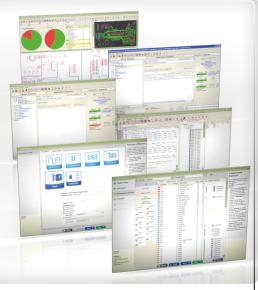
Boundary Scan (IEEE Std. 1149.x) is a revolutionary technology substituting physical access via nails and probes by means of special on-chip electronics (electronic nails) in conjunction with a dedicated four-wire bus. Meanwhile, the method has been well established as an integral part of the so called Embedded System Access (ESA), which unites all non-contact methods.

SCANFLEX Designer Studio provides a cost-efficient solution for utilization of ESA for complex projects which still contains all necessary features for powerful and convenient test program generation, execution and failure diagnosis incl. interactive debugger. The system can be easily upgraded at any time.

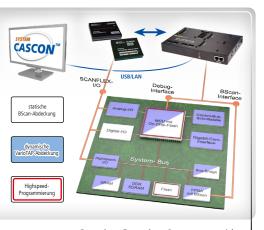


SFX/COMBO 1149 at a glance 4 TAP via TIC 020 (1x) 8 TIC 01 (3x) TAP Interface Card TIC 01 TAP Interface Card TIC 020 (VarioTAP) SFX/LS Extension Bus

SCANFLEX-Controller for USB 2.0 / GigE



Graphical development tools in SYSTEM CASCON™



Complete Boundary Scan tester with option for CION Modules



ISO 9001 certified

SCANFLEX Designer Studio

Hardware and Software Components

SCANFLEX Designer Studio provides an excellent price-performance ratio by utilizing a convenient controller in combination with a special edition (Base/SX or Standard/SX) of the worldwide leading Boundary Scan software platform SYSTEM CASCON™. Features include:

- SCANFLEX controller (USB 2.0/GigE) with TIC 020/01
- Programmable VIO (1.8 V 5 V) and impedance
- Add-on analogue and digital I/O
- SYSTEM CASCON platform license incl. run-time
- All CAD Readers and Device Libraries
- Design and Testability Explorer
- Multi Board Merger
- Mission Assist for graphical project development
- Automated test generation for interconnections
- Automated pin-failure diagnosis for interconnections
- Unlimited manual test generation incl. diagnosis
- ScanAssist[™]: Interactive Pin Toggler
- ScanAssist™: Multi Mode Debugger
- Programming of PLDs and moderate Flash*
- Graphical Test Coverage Analyzer (TCA)*
- ATPG/pin-failure-diagnosis for memories and clusters*
- ScanVision[™] layout visualizer (with single CAD reader)²
- One-year maintenance contract
 - * available in "Standard Version" only / 2 available in "Comfort Version" only

The package is well prepared for a multitude of Boundary Scan test applications. Depending on the demands, it can be extended even to high-end applications.

Extension in various Directions

Both hardware and software can be upgraded at any time.

Particular options to be named are:

- Different TIC modules (e.g. for greater UUT distances)
- SFX I/O modules (e.g. for connector tests)
- Additional Automated Test Generators (ATPG)

The enhancement options are activated easily by a new license file.

Product Support and Oder Information

SCANFLEX Designer Studio includes a one-year maintenance contract. This ensures access to the worldwide support and service network. Software updates are available free of charge.

Product	CASCON Package	Product Number
SCANFLEX Designer Studio Basic Version	DS Base/SX	SHB-040
SCANFLEX Designer Studio Standard Version	DS Standard/SX	SHB-041
SCANFLEX Designer Studio Comfort Version	DS Standard	SHB-041

Additional information can be found at **goepel.com**.

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